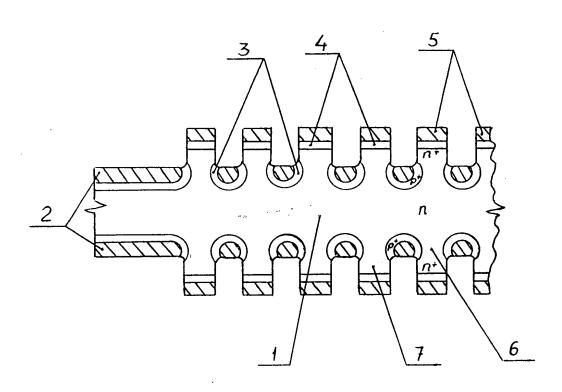


ФИГ.1

ABTOP

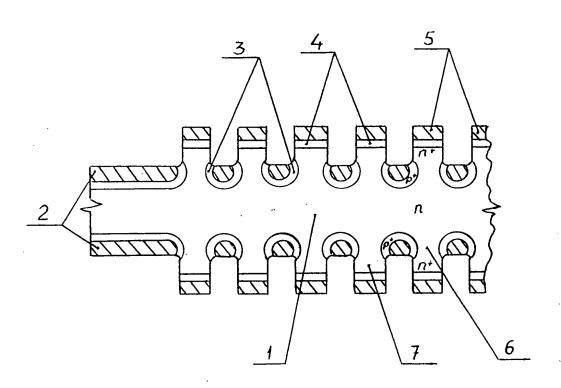
эдлин с.д.



ФИГ.1

ABTOP

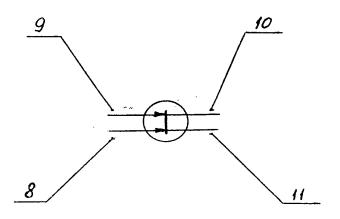
эдлин с.д.



ФИГ.1

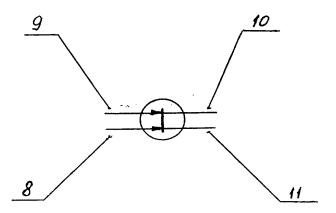
ABTOP

эдлин с.д.



ФИГ.2

ABTOP

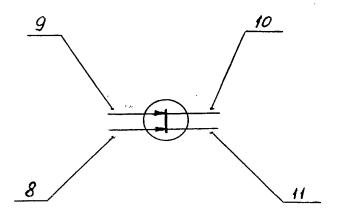


ФИГ.2

ABTOP

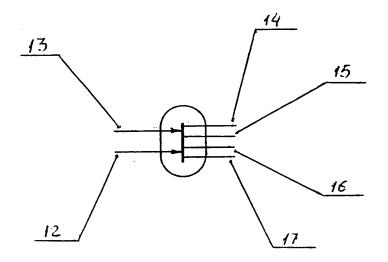
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БИПОЛЯРНЫЙ ТРАНЗИСТОР СО СТАТИЧЕСКОЙ ИНДУКЦИЕЙ (ВАРИАНТЫ)



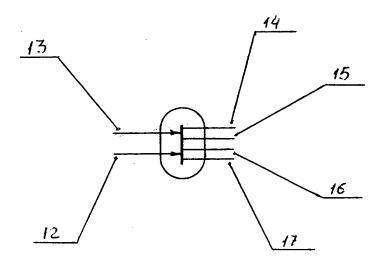
ФИГ.2

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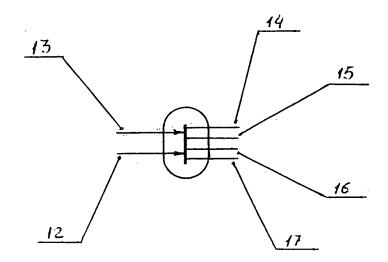
ФИГ.3

ABTOP



ФИГ.3

ABTOP



ФИГ.3

ABTOP